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Dated: June 20, 2005

Signature:

(Judith A. Hermick)

Docket No.: YU-P07-002  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Reed et al.

Confirmation No.: 4089

Application No.: 10/643747

Art Unit: 1626

Filed: August 18, 2003

Examiner: Kamal A. Saeed

For: SUB-NANOSCALE ELECTRONIC DEVICES  
AND PROCESSES

**AMENDMENT UNDER 37 CFR 1.312**

MS Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

**INTRODUCTORY COMMENTS**

This amendment under 37 CFR 1.312 is submitted with payment of the issue fee to correct certain omissions and errors in the Examiner's amendment dated 3/24/2005. Please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks/Arguments** begin on page 4 of this paper.